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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	<b>Application Number</b>	10/079,336
	<b>Filing Date</b>	2/19/2002
	<b>First Named Inventor</b>	Vladislav Vashchenko
	<b>Group Art Unit</b>	2836
	<b>Examiner Name</b>	Boris Benenson
<b>Total Number of Pages in This Submission</b>	<b>Attorney Docket Number</b>	P05143

ENCLOSURES (check all that apply)		
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<b>Remarks</b>		

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Firm or Individual name	Jurgen Vollrath
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Jurgen Vollrath

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	)	
	)	Art Unit: 2836
Inventor: Vladislav Vashchenko	)	
	)	
Serial No.: 10/079,336	)	
	)	Examiner: Boris Benenson
Filed: February 19, 2002	)	
	)	
Title: <b>ESD PROTECTION METHODS</b>	)	
<b>AND DEVICES USING ADDITIONAL</b>	)	
<b>TERMINAL IN THE DIODE STRUCTURES</b>	)	

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

## RESPONSE TO OFFICE ACTION

The examiner is thanked for the Office action that was re-mailed 5/11/2004.